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TL Borden, JP Hennessy, JW Rymarczyk - IBM Systems Journal, 1989 - research.ibm.com

 ... LPAR gives the user the ability to define the granularity of the partitions. A **logical partition** is a collection of processor com- plex resources that, when ...

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D Pendarakis, J Silber, L Wynter - doi.ieeecomputersociety.org

 ... the **percentage** of (local) link bandwidth allocated to process i. Naturally,  $i=1...n$   $b_i \leq 1$ . In general, CPU utilization of any process,  $i$ ,  $c_i : n$  ...

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D Turk, J Bausch - IBM Systems Journal, 2005 - research.ibm.com

 ... supports logical partitioning (a **logical partition** is referred ... z/VM guests running within this LPAR. ... processed, each is allocated one (processor) **time slice**. ...

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IBM Certification Study Guide AIX Version 4.3 Performance and System Tuning

TC Cederlöf, A de Klerk, T Herlin, T Ostaszewski - e-techservices.com

... Chapter 8. CPU testcase . . . . Enable, disable, and show status of processors • List CPU utilization per processor • Know about ps command and threads ...

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WJ Rooney, JP Kubala, J Maergner, P Yocom - IBM Journal of Research and Development, 2002 - research.ibm.com

 ... the average weight of the members of the LPAR cluster ... processor demand represents the **percentage** of the overall CPU capacity of the **logical partition** that a ...

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A Troelsen - 2001 - dotnetforum.dk

 ... The chances are almost 100 **percent** that the code you write at ... Rather, a single CPU will execute one thread for a unit of time (called a **time-slice**) based on ...

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A Hoefzel - 1998 - status.lsu.edu

 ... 1.1.4.5 Access to Larger Physical Memory As mentioned before, the physical addresses that a 64-bit CPU generates are up ... There is 100 **percent** compatibility in 32 ...

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HH Chu, K Nahrstedt - Multimedia Computing and Systems, 1999. IEEE International ... 1999 - [ieeexplore.ieee.org](#)

... as a percentage of the total processor **resource** which is ... a top-level scheduler which al- locates processor time to ... Let Tu be the length of **time slice** used by ...

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I Stoica, H Abdel-Wahab, K Jeffay, S Baruah, J ... - Proceedings of the IEEE RTSS, 1996 - [doi.ieeecs.org](#)

... On the one hand, proportional share **resource allocation** is a variant of the pure proces ... each time unit each process receives 1/n of the processor's capacity ...

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JK Millen - Research in Security and Privacy, 1992. Proceedings., 1992 ... 1992 - [ieeexplore.ieee.org](#)

... **Resource allocation** The relationship between "services" and ... Any system with **time-slice** scheduling, for example ... and reassigns) access to the **CPU resource**. ...

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**On the Duality between Resource Reservation and Proportional Share Resource Allocation - group of 8 »**

I Stoica, H Abdel-Wahab, K Jeffay - Multimedia Computing and Networking Proceedings, SPIE ... 1997 - [cs.unc.edu](#)

... client reserves 50% of the **CPU**, the remaining ... bid acquires the **resource** for the next **time-slice**. ... scheme successfully solves the **resource allocation** problem in ...

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**book A Practitioner's Handbook for Real-Time Analysis: Guide to Rate Monotonic Analysis for Real-Time ...**

MH Klein, T Ralya, B Pollak, R Obenza, MG Harbour - 1993 - Kluwer Academic Publishers

... Response 7-12 Situation 18 Actions at a Higher Priority 7-14 Situation 19

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**SMART (strategic memory allocation for real-time) cache design**

DB Kirk - Real Time Systems Symposium, 1989., Proceedings., 1989 - [ieeexplore.ieee.org](#)

... Caches have been bridging the gap between **CPU** speeds and ... this static time-slicing of system **resources** is shown ... are processed by dedicat- ing a **time slice** to a ...

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MA Jette - Proc. of Supercomputing - [doi.ieeecomputersociety.org](#)

... is also permitted to alter its **resource** requirements during ... use which is updated at **time-slice** boundaries. ... For example, if a program's **CPU allocation** and **CPU** ...

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Eleftherios D. Polychronopoulos, Xavier Martorell, Dimitrios S. Nikolopoulos, Jesus Labarta, Theodore S. Papatheodorou, Nacho Navarro

July 1998

**Proceedings of the 12th international conference on Supercomputing**

Publisher: ACM Press

Full text available: pdf (1.20 MB)

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### 22 [The impact of operating system scheduling policies and synchronization methods of performance of parallel applications](#)

Anoop Gupta, Andrew Tucker, Shigeru Urushibara

April 1991

**ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1991 ACM SIGMETRICS conference on Measurement and modeling of computer systems SIGMETRICS '91**, Volume 19 Issue 1

Publisher: ACM Press

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Shared-memory multiprocessors are frequently used as compute servers with multiple parallel applications executing at the same time. In such environments, the efficiency of a parallel application can be significantly affected by the operating system scheduling policy. In this paper, we use detailed simulation studies to evaluate the performance of several different scheduling strategies. These include regular priority scheduling, coscheduling or gang scheduling, process control with processor pa ...

### 23 [Borrowed-virtual-time \(BVT\) scheduling: supporting latency-sensitive threads in a general-purpose scheduler](#)

Kenneth J. Duda, David R. Cheriton

December 1999

**ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles SOSP '99**, Volume 33 Issue 5

Publisher: ACM Press

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Systems need to run a larger and more diverse set of applications, from real-time to interactive to batch, on uniprocessor and multiprocessor platforms. However, most